WEST Search History

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DATE: Sunday, March 05, 2006

Hide?	<u>Set</u> Name	Query .	<u>Hit</u> Count
DB=PGPB; PLUR=YES; OP=ADJ			
	L5	((peripheral device and dynamically mapping and dynamically unmappable).clm.)	0
	DB =	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ	
	L4	((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5 or deallocat\$4 or de-allocat\$ or unallocat\$4 or un-allocat\$ or remov\$ or delet\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4)))	
	L3	((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5 or deallocat\$4 or de-allocat\$ or unallocat\$4 or un-allocat\$ or remov\$ or delet\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4)))	
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	L1	(((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5 or deallocat\$4 or de-allocat\$ or unallocat\$4 or un-allocat\$ or remov\$ or delet\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4))).ab,ti,bsum. and (memory or memories or storage) and (cpu or processor or coprocessor or microprocessor or host) not ((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5 or deallocat\$4 or de-allocat\$ or unallocat\$4 or un-allocat\$ or remov\$ or delet\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4))).ab,ti,bsum. and (memory or memories or storage) and (cpu or processor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or oprocessor) and (demap\$5 or de-map\$5 or un-map\$5 or unmap\$5) or ((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5) or unmap\$5 or unallocat\$4 or un-allocat\$ or remov\$ or delet\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4))).ab,ti,bsum. and (memory or memories or storage) and (cpu or processor or coprocessor or microprocessor or host) not ((demap\$5 or de-map\$5 or un-map\$5 or unmap\$5 or unmap\$5 or eliminat\$) ADJ3 (map or maps or translation table or TLB or (translation near2 index\$4))).ab,ti,bsum. and (memory or memories or storage) and (cpu or processor or coprocessor or coprocessor or microprocessor or storage) and (cpu or processor or coprocessor or microprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or coprocessor or microprocessor or host) and (coprocessor or coprocessor or coprocessor or coprocessor or coprocessor or coprocessor or cop	41

END OF SEARCH HISTORY